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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/702,238	11/06/2003	Bradley J. Howard	97-0008.02	7948
7	590 08/25/2004		EXAMINER	
Richard D. Egan			LEE, SIN J	
O'KEEFE, EGAN & PETERMAN Building C, Suite 200 1101 Capital of Texas Highway South			ART UNIT	PAPER NUMBER
			1752	
Austin, TX 78746			DATE MAILED: 08/25/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	— ( )		
	10/702,238	HOWARD, BRADLEY	J.		
Office Action Summary	Examiner	Art Unit			
	Sin J. Lee	1752			
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet w	ith the correspondence addres	5S		
A SHORTENED STATUTORY PERIOD FOR REPL' THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period of Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a y within the statutory minimum of thin will apply and will expire SIX (6) MON	reply be timely filed  ty (30) days will be considered timely.  THS from the mailing date of this community.	inication.		
Status					
1) Responsive to communication(s) filed on <u>06 N</u>					
	action is non-final.				
3) Since this application is in condition for allower	nce except for formal matt	ers, prosecution as to the me	rits is		
closed in accordance with the practice under E	x paπe Quayle, 1935 C.D	·. 11, 453 O.G. 213.			
Disposition of Claims					
4) Claim(s) 55-100 is/are pending in the application	on.				
4a) Of the above claim(s) is/are withdraw	vn from consideration.				
5) Claim(s) is/are allowed.					
6) Claim(s) is/are rejected.					
7) Claim(s) is/are objected to.	. 1- 0				
8) Claim(s) <u>55-100</u> are subject to restriction and/o	r election requirement.				
Application Papers					
9)☐ The specification is objected to by the Examiner					
10)☐ The drawing(s) filed on is/are: a)☐ acce	epted or b) objected to t	by the Examiner.			
Applicant may not request that any objection to the o	frawing(s) be held in abeyan	ce. See 37 CFR 1.85(a).			
Replacement drawing sheet(s) including the correcti	on is required if the drawing(	s) is objected to. See 37 CFR 1.1	121(d).		
11)☐ The oath or declaration is objected to by the Exa	aminer. Note the attached	Office Action or form PTO-15	52.		
Priority under 35 U.S.C. § 119					
12)☐ Acknowledgment is made of a claim for foreign all all ble Some * clear None of:	priority under 35 U.S.C. §	119(a)-(d) or (f).			
<ol> <li>Certified copies of the priority documents</li> </ol>	have been received.				
2. Certified copies of the priority documents	have been received in Ap	oplication No			
3. Copies of the certified copies of the priori	ty documents have been	received in this National Stage	е		
application from the International Bureau					
* See the attached detailed Office action for a list of	of the certified copies not r	eceived.			
Attachment(s)					
1) Notice of References Cited (PTO-892)	∧ □ 1-1i o				
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  Paper No(s)/Mail Date.					
Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)     Paper No(s)/Mail Date	5)  Notice of Inf 6)  Other:	formal Patent Application (PTO-152)			

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## Election/Restrictions

Restriction to one of the following inventions is required under 35 U.S.C. 121:

- I. Claims 55-58, 76-83, 87, drawn to a process of using a photo-definable layer in manufacturing a semiconductor device, classified in class 430, subclass 313.
- II. Claims 62-70, drawn to a process for forming a self-aligned contact, classified in class 430, subclasses 314 and 317.
- III. Claims 88-96, drawn to a process of using a photo-definable layer in a dual Damascene process, classified in class 430, subclass 312.
- IV. Claims 59-61, 71-75, 84-86, 97-100, drawn to a semiconductor device, classified in class 430, subclass 319.
- 2. Inventions I and II are unrelated. Inventions are unrelated if it can be shown that they are not disclosed as capable of use together and they have different modes of operation, different functions, or different effects (MPEP § 806.04, MPEP § 808.01). In the instant case, the process of Invention II requires material deposition whereas Invention I does not require such step.
- 3. Inventions I and III are unrelated. Inventions are unrelated if it can be shown that they are not disclosed as capable of use together and they have different modes of operation, different functions, or different effects (MPEP § 806.04, MPEP § 808.01). In the instant case, the process of Invention III requires multiple imaging (exposure) steps whereas Invention I does not require such step.

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- 4. Inventions II and III are unrelated. Inventions are unrelated if it can be shown that they are not disclosed as capable of use together and they have different modes of operation, different functions, or different effects (MPEP § 806.04, MPEP § 808.01). In the instant case, process of Invention II requires material deposition whereas Invention III does not require such step. Also, the process of Invention III requires multiple imaging (exposure) steps whereas Invention II does not require such step.
- 5. Inventions I and IV are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case, the semiconductor device in Invention IV can be made by using organic photoresist material and a separate insulative material instead of using the present photodefinable layer that is convertible to an insulative material.
- 6. Inventions II and IV are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case, the semiconductor device in Invention IV can be made by using organic photoresist material and a separate insulative material instead of using the present photodefinable layer that is convertible to an insulative material.

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- 7. Inventions III and IV are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case, the semiconductor device in Invention IV can be made by using organic photoresist material and a separate insulative material instead of using the present photodefinable layer that is convertible to an insulative material.
- 8. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.
- 9. Because these inventions are distinct for the reasons given above and the search required for Group I is not required for Group II-IV (the search required for Group III is not required for Group III), restriction for examination purposes as indicated is proper.
- 10. Due to complexity of the restriction, a telephone call was not attempted.
- 11. Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).
- 12. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sin J. Lee whose telephone number is 571-272-1333.

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The examiner can normally be reached on Monday-Friday from 9:00 am EST to 5:30 pm EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Cynthia Kelly, can be reached on 571-272-1526. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

S.J.L.

S. Lee August 23, 2004 Sin J. Lee

Patent Exammer

Technology Center

1700